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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: John W. Rapp, Larry Jackson, Mark Jones, and Troy Cherasaro

Title: PIPELINE ACCELERATOR FOR IMPROVED COMPUTING ARCHITECTURE AND RELATED SYSTEM AND METHOD

Serial Number: 10/683,929

Filing Date: October 9, 2003

Examiner/Unit: / 2818

Attorney Docket No.: 1934-13-3



CERTIFICATE OF MAILING OR TRANSMISSION

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Signature

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ACCOMPANYING SECTION 1.97(e) CERTIFICATION**

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In compliance with 37 CFR § 1.56 and § 1.97, Applicants acting by and through the attorney of record, hereby discloses patents and other documents listed on the attached Form PTO-1449 which may be considered relevant to the examination of the subject application.

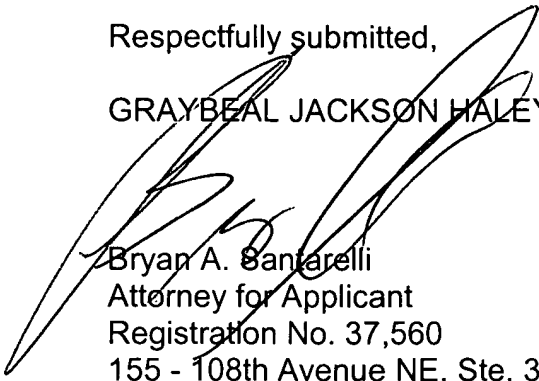
A copy of the cited non-U.S. reference is enclosed.

Certification Under Section 1.97(e)

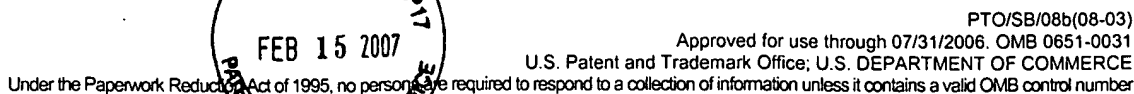
Each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign Patent Office in a counterpart foreign application not more than three months prior to the filing of this statement.

Respectfully submitted,

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<i>Application Number</i>	10/683,929
<i>Filing Date</i>	October 9, 2003
<i>First Named Inventor</i>	John W. Rapp
<i>Art Unit</i>	2818
<i>Examiner Name</i>	
<i>Attorney Docket Number</i>	1934-13-3

[illegible]

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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